

Fig.1

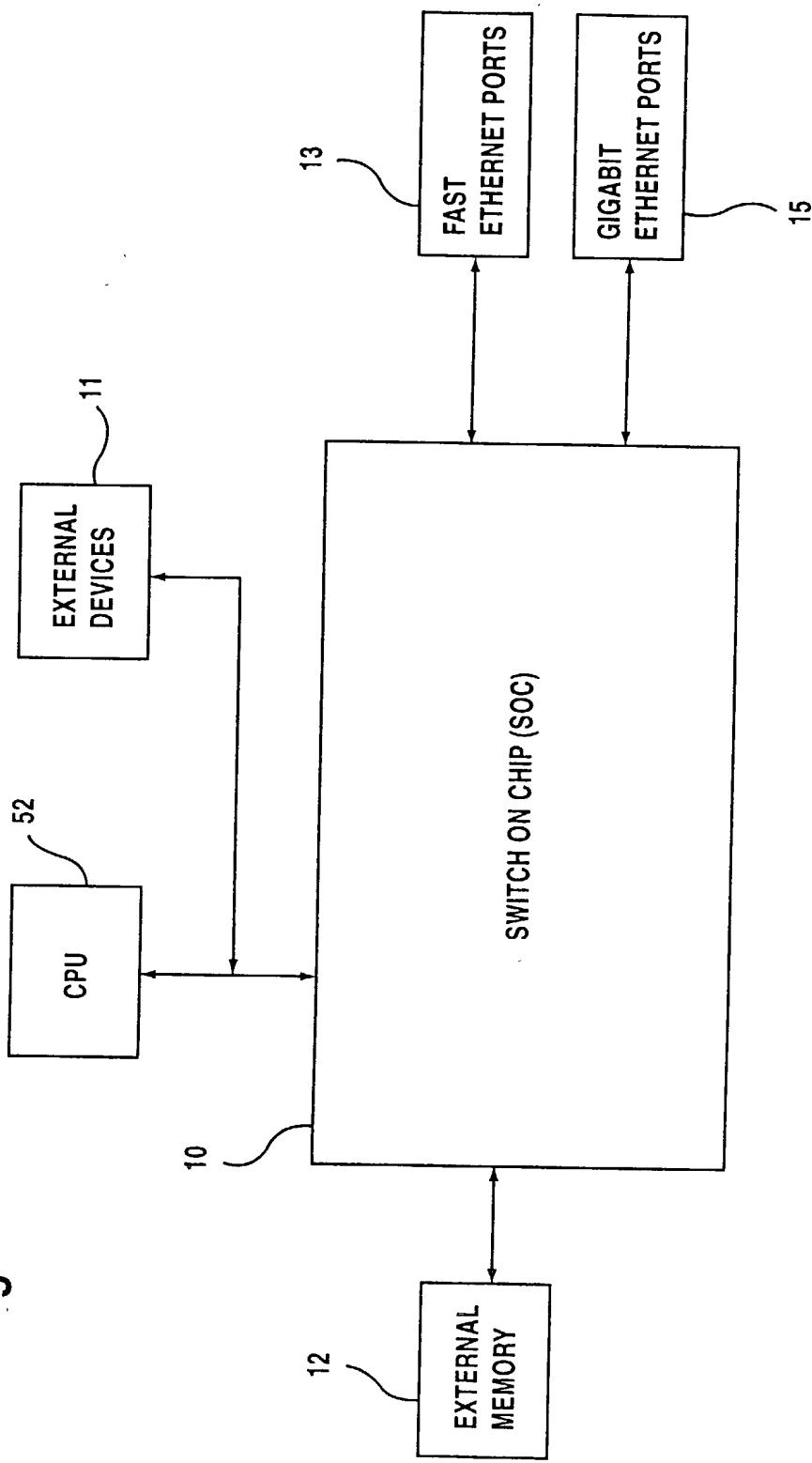


Fig.2

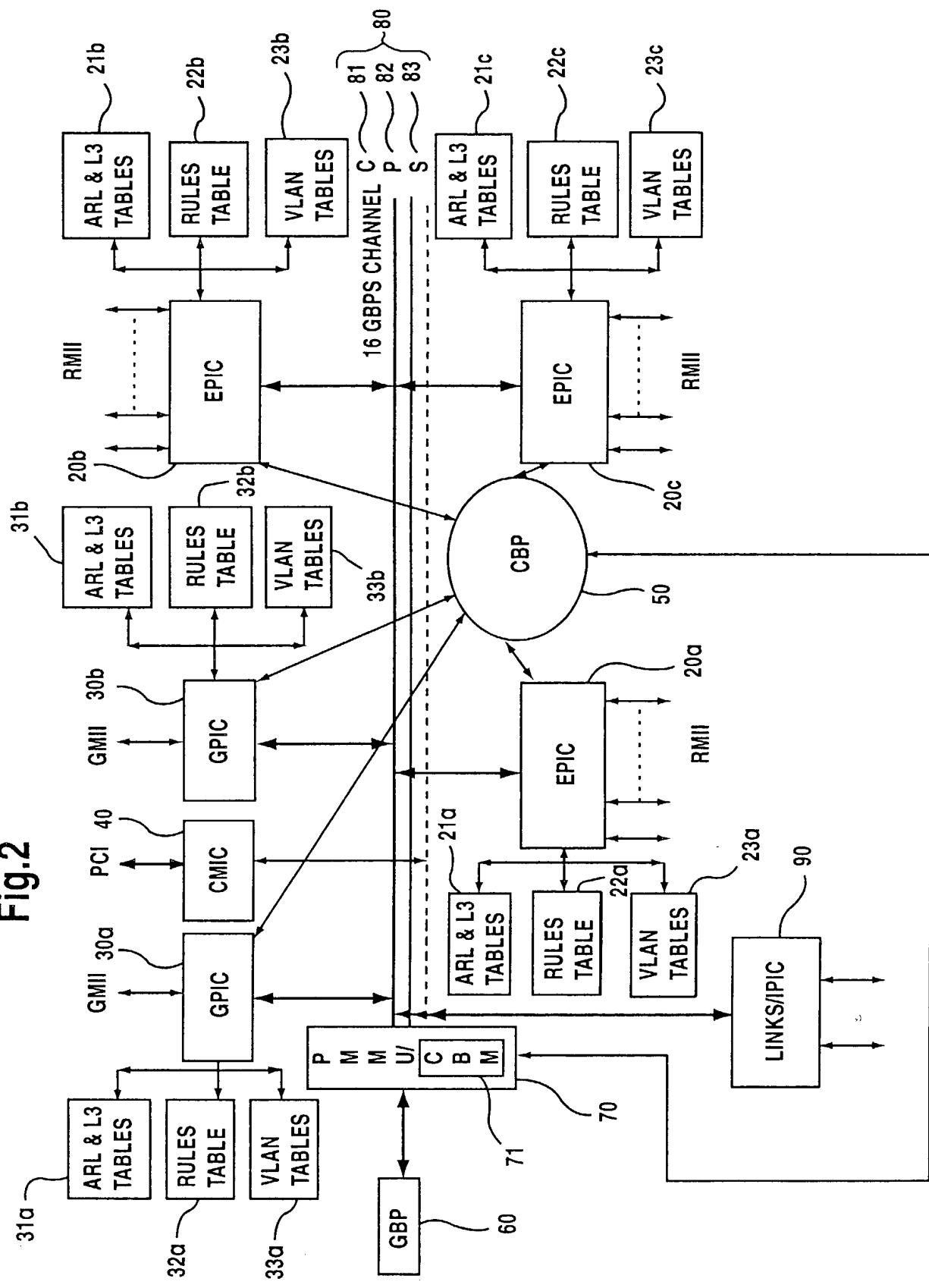


Fig.3

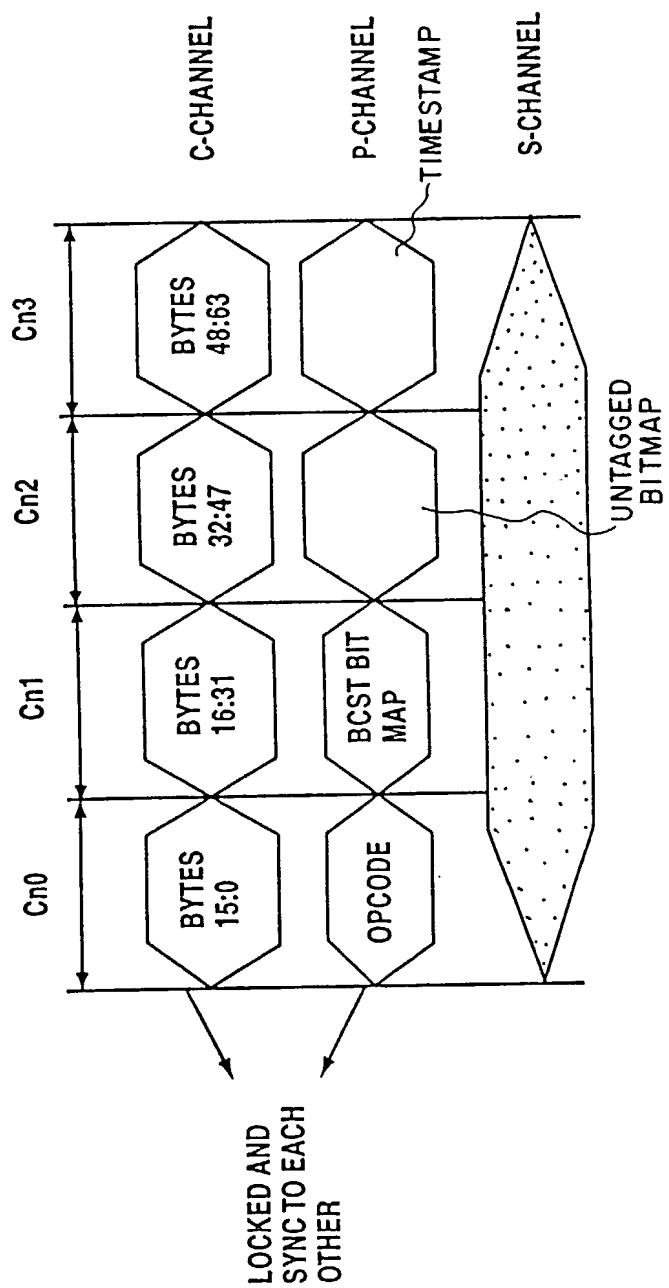


Fig.4

PROTOCOL CHANNEL MESSAGES

OP CODE		P P X		RESERVED		SRC DEST PORT		COS		J S E CR C		P O		LEN	
30	28	26	24	22	20	18	16	14	12	10	8	6	4	2	0

CPU OPCODES										TIME STAMP					
30	28	26	24	22	20	18	16	14	12	10	8	6	4	2	0

Fig.5

SIDE BAND CHANNEL MESSAGES

Fig.6

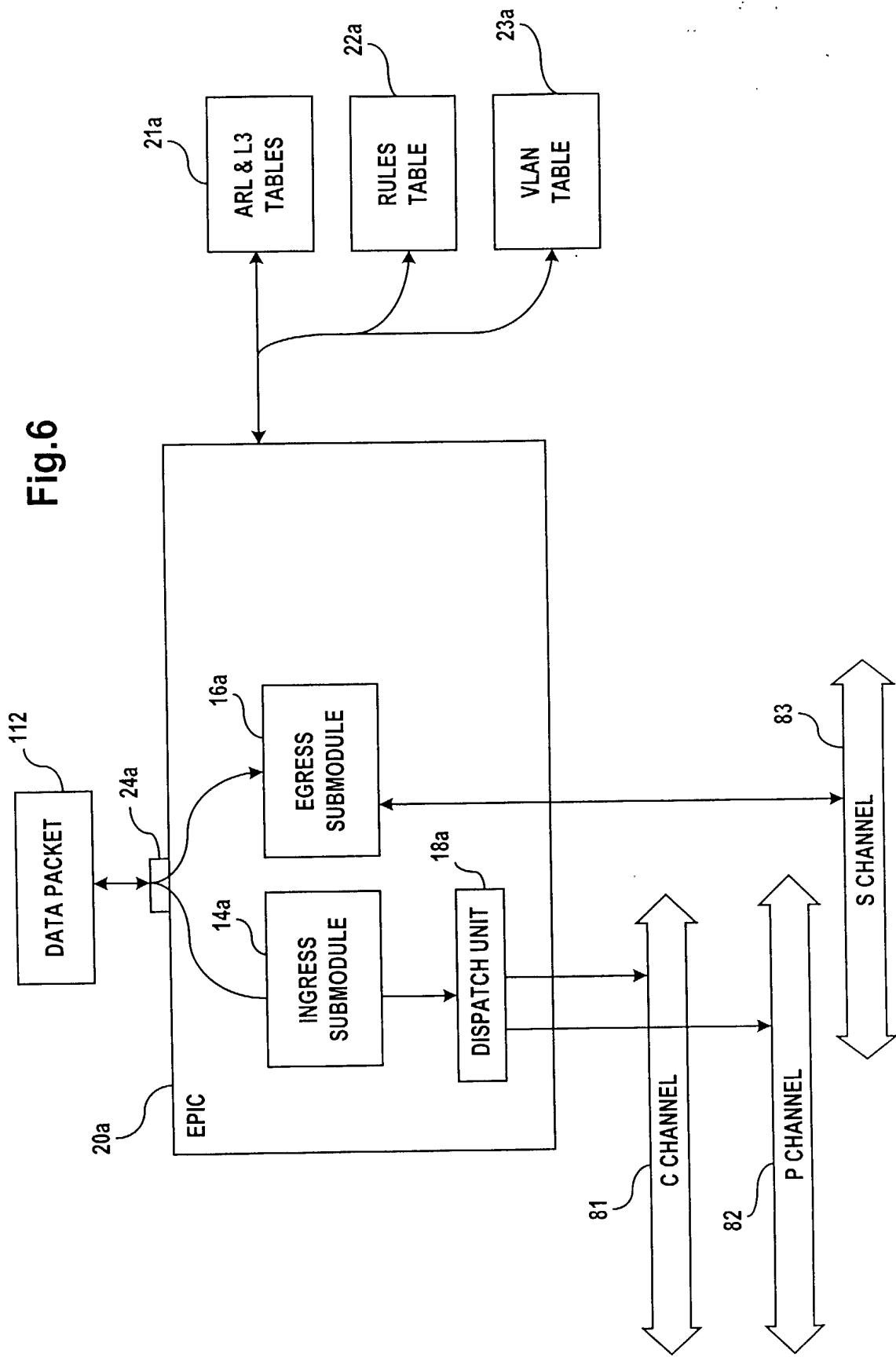


Fig.7

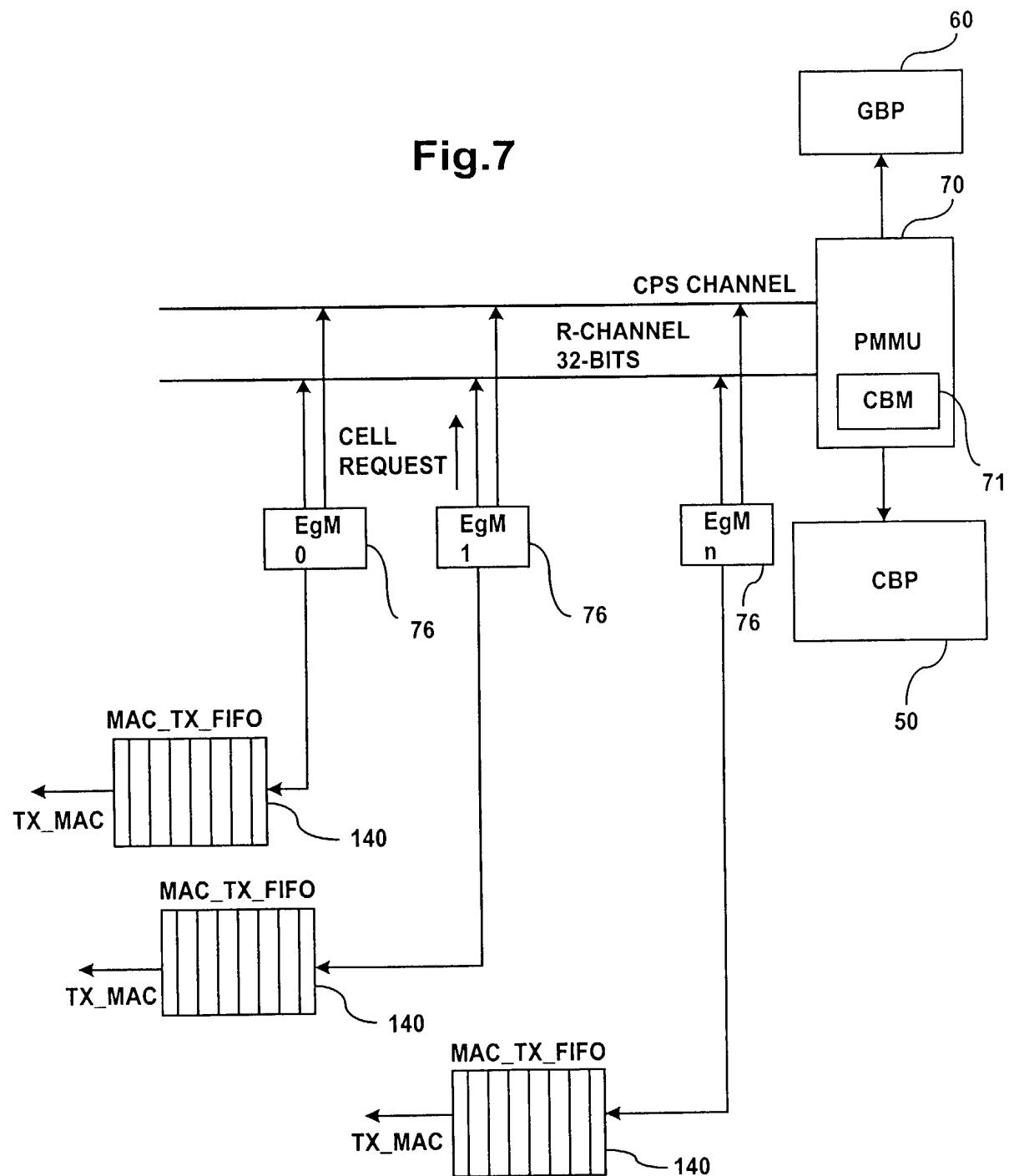
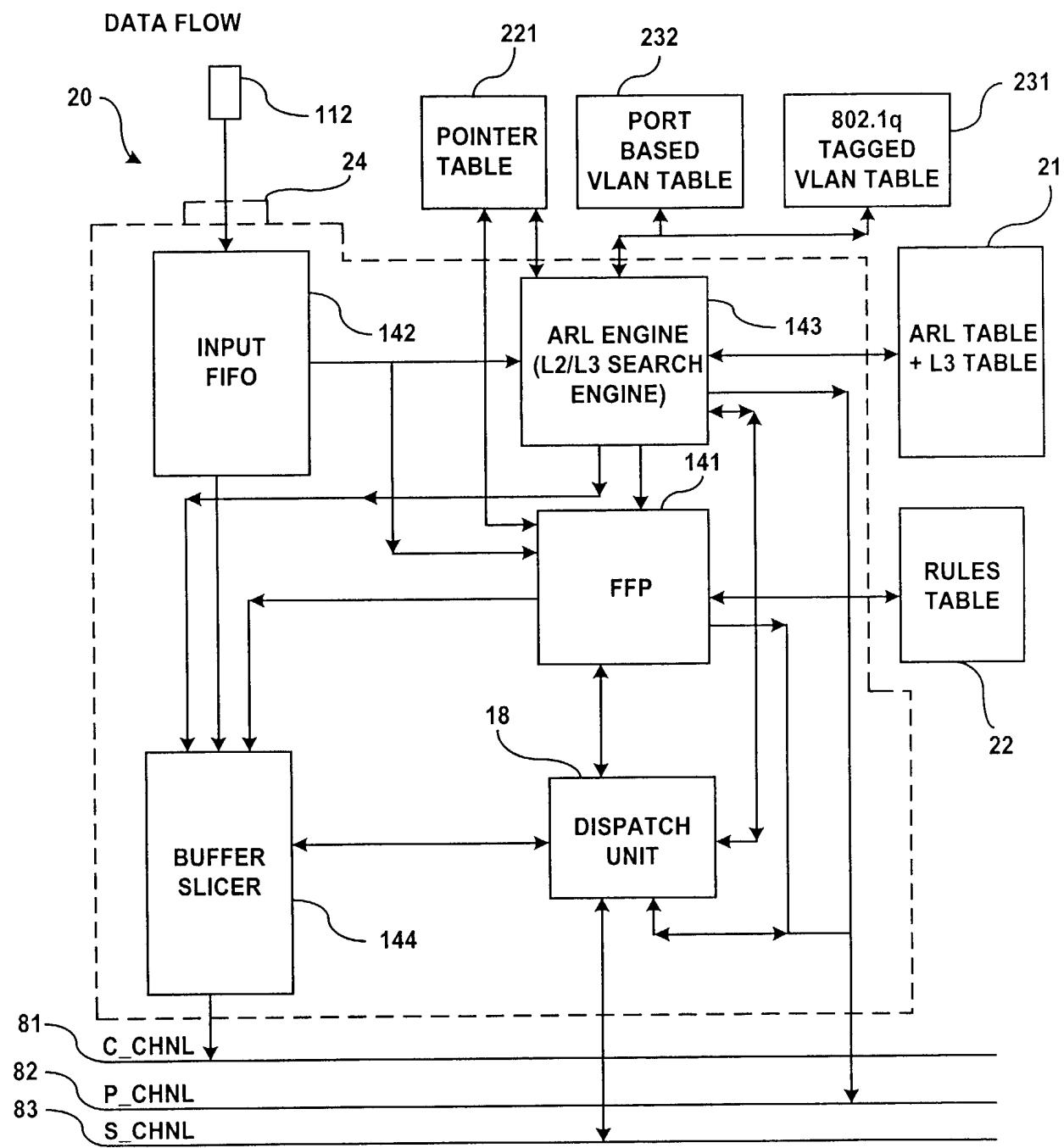


Fig.8



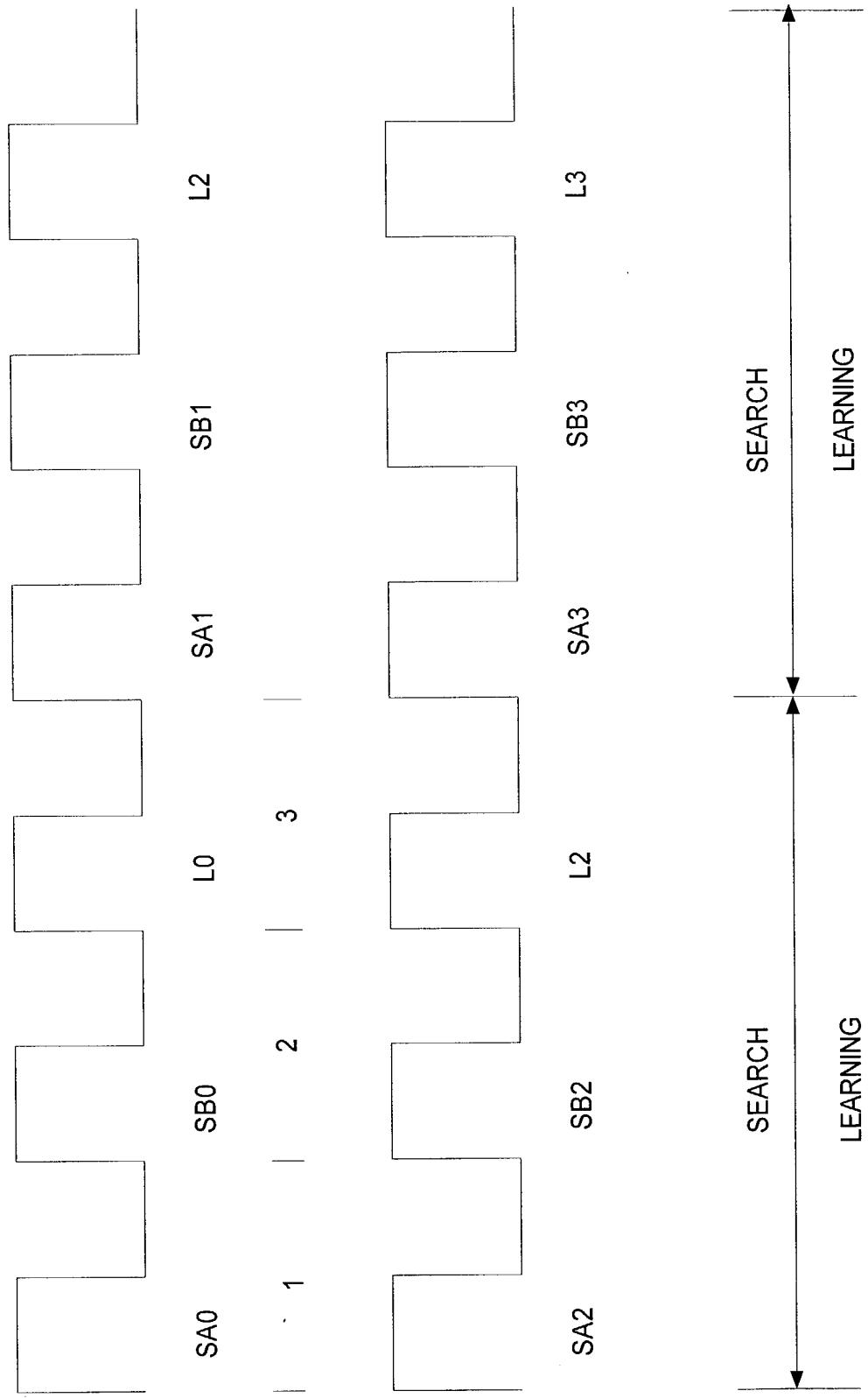


FIG 9

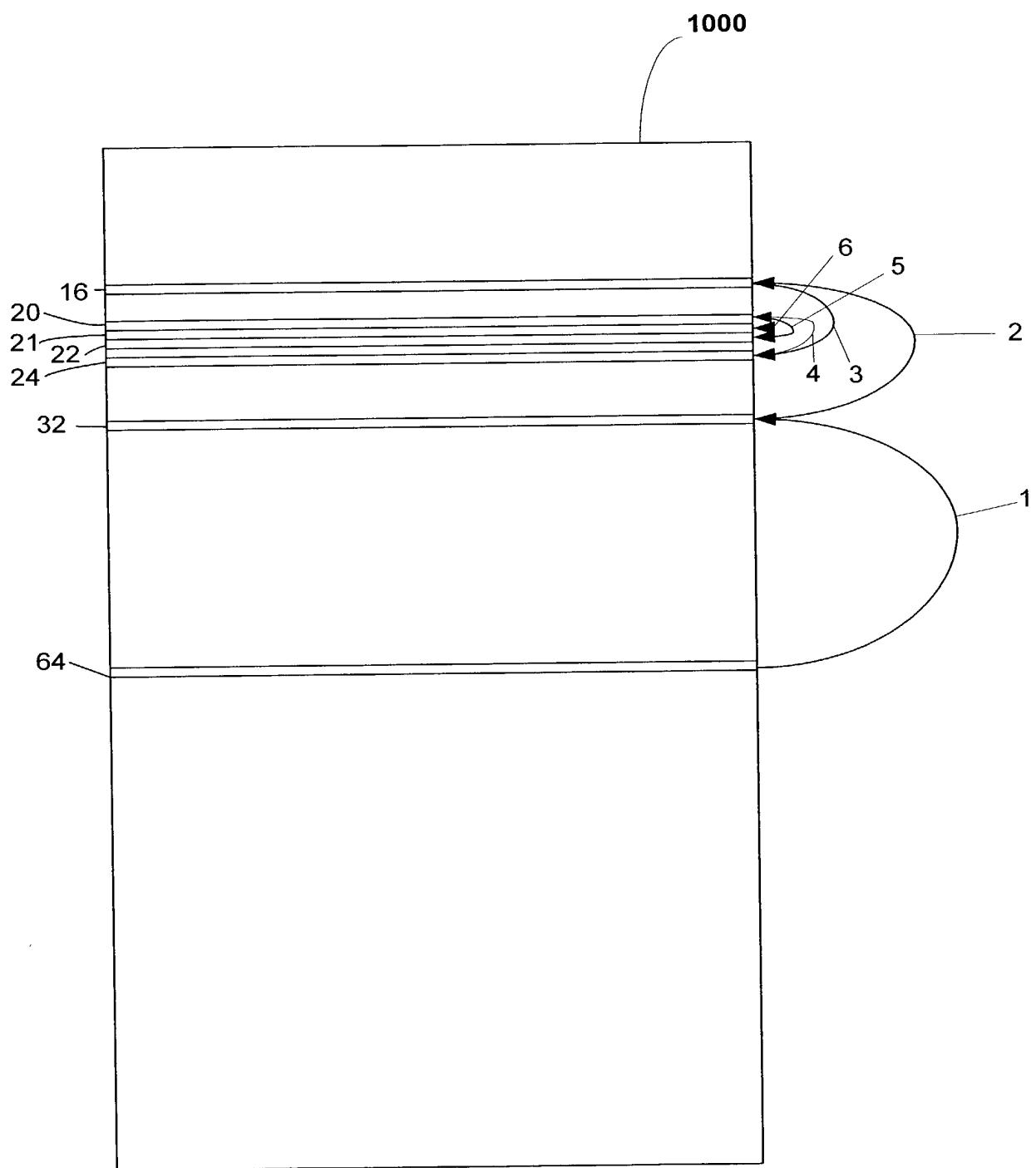
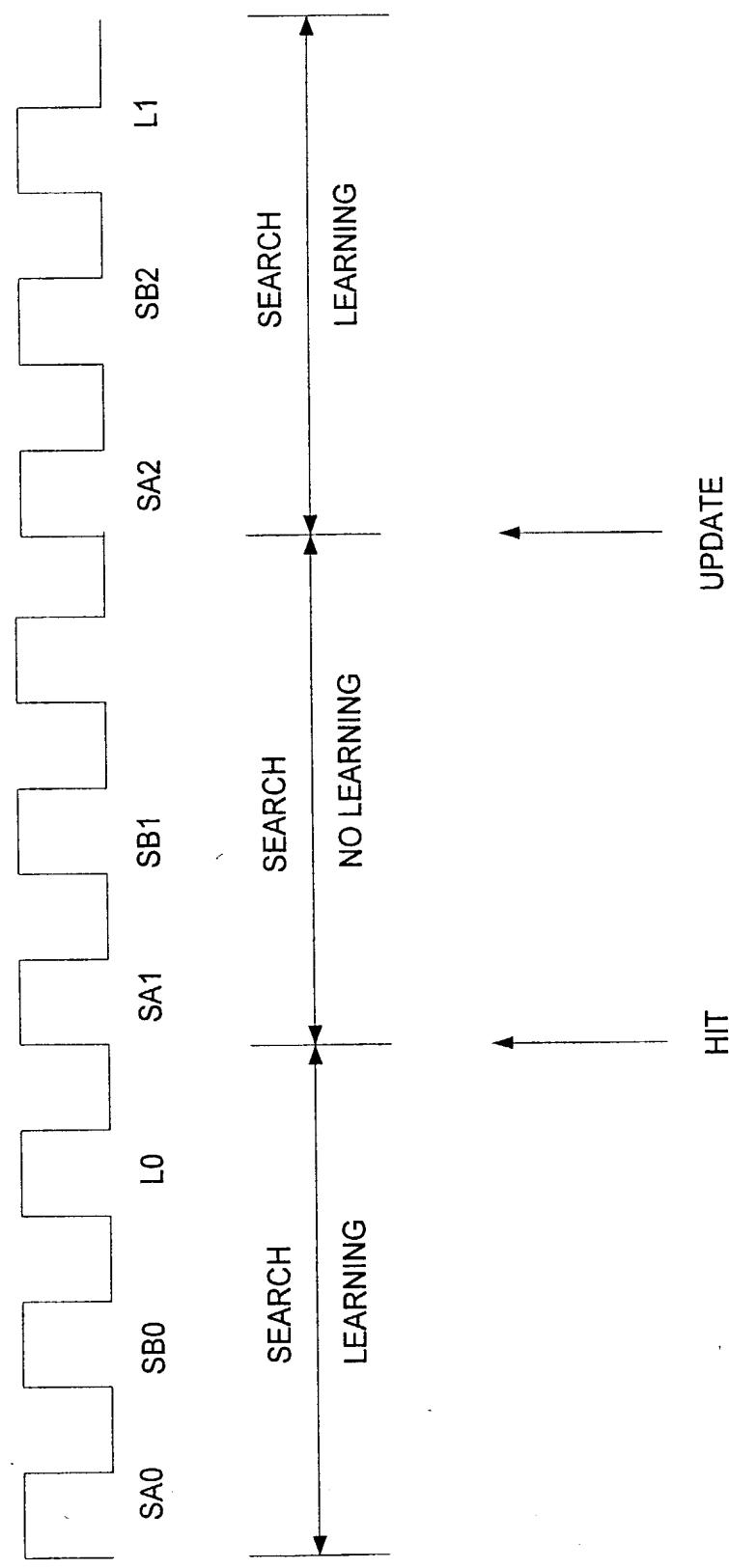


Figure 10

FIG 11



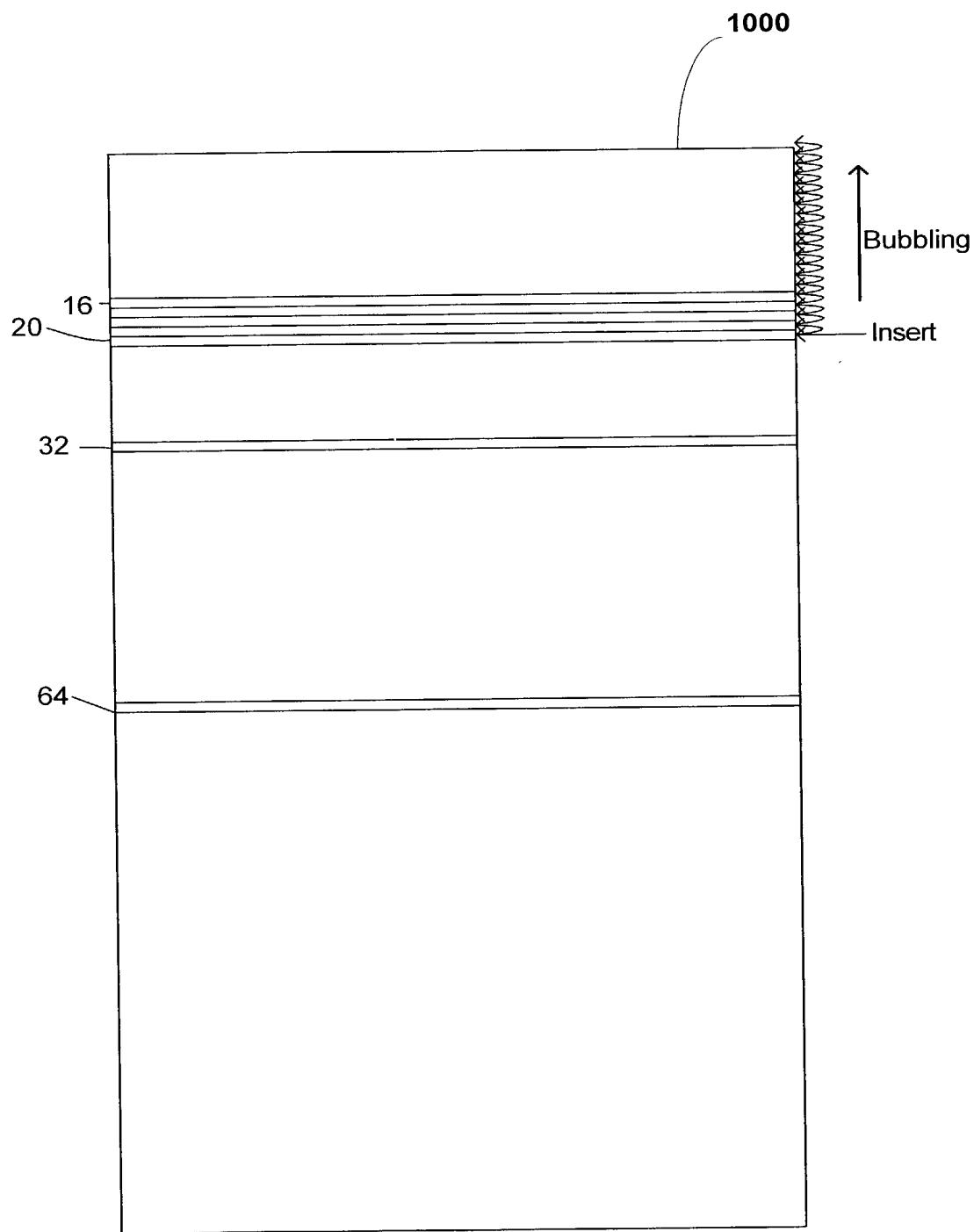


Figure 12

FIG. 13

Step	1	3	4	5	6	7	8	9	2
Index	256	320	352	368	376	380	382	383	384
Key	1000	1300	1400	1500	1600	1700	1800	1900	2000

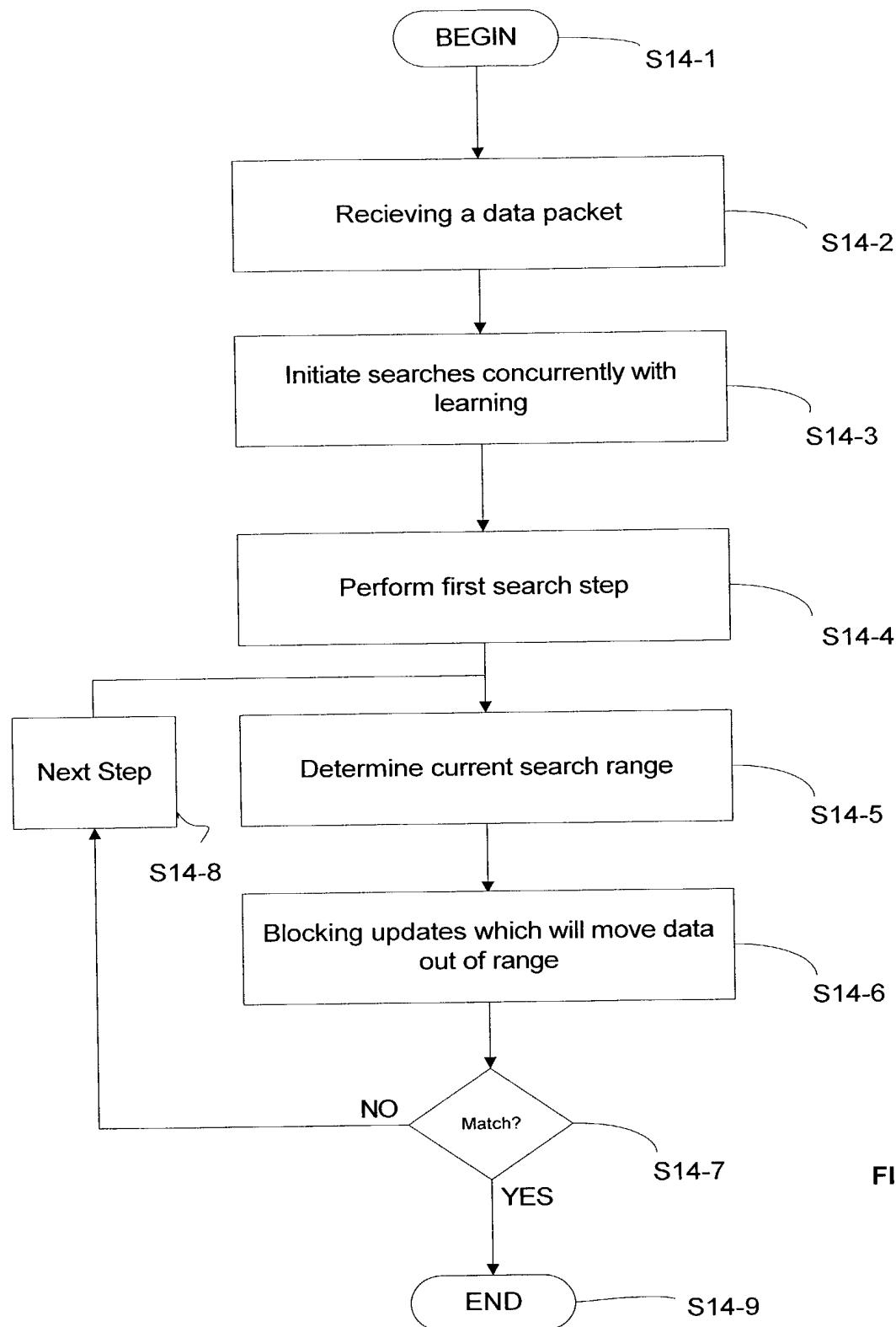


FIG. 14